**17EC4101-VLSI DESIGN**

**(ECE)**

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| **Course category:** | Open Elective | **Credits:** | 3 |
| **Course Type:** | Theory |  **Lecture - Tutorial - Practical:** | 3 - 0- 0 |
| **Pre-requisite:** | Electronic Devices & Circuits, Linear & Digital ICs and Basics of IC fabrication | **Sessional Evaluation :****External Evaluation:****Total Marks:** | 4060100 |

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| **Course****Objectives:** | Students undergoing this course are expected: |
| 1. To learn the fundamental structures of VLSI Systems at the lowest levels of  System abstraction.2. To learn the basic electrical properties of MOS & BI-CMOS circuits3. To learn the basic circuit concepts and design process of VLSI circuits and also  to introduce the fundamental principles of VLSI circuit design.4. To learn the gate level design and physical design by considering partioning, floor  planning, placement and routing.5. To bring both circuits and system views on design together by considering circuit  Subsystems and VLSI Design styles. 6. To learn the design of complex digital VLSI circuits, computer aided simulation  and synthesis tool for  hardware design |
| **Course Outcomes:** | Upon successful completion of the course , the students will be able to: |
| **CO1** | Understand the trends in semiconductor technology, and its impacts scaling and performance. |
| **CO2** | Understand the basic electrical properties of MOS & BI-CMOS circuits. |
| **CO3** | Understand layout, stick diagrams, fabrication steps, static and switching characteristics of inverters. |
| **CO4** | Compute terminal voltage and current characteristics for MOS transistors under a variety of conditions. |
| **CO5** | Understand MOS transistor as a switch and its capacitance. |
| **CO6** | Understand design digital systems using MOS circuits synthesis of digital VLSI systems from register-transfer or higher-level descriptions in hardware design languages. |
| **Course****Content:** | **UNIT-I****Introduction:** IC fabrication, MOS, PMOS, NMOS, CMOS & Bi-CMOS technologies, oxidation, lithography, diffusion, ion implantation, metallization, encapsulation, probe testing, integrated resistors and capacitors.**UNIT-II****Basic electrical properties of MOS & Bi-CMOS circuits:** Ids-Vds relationships, MOSFET threshold voltage, gm, gds, Wo, Pass transistor, NMOS Inverter, various pull ups, CMOS inverter analysis and design bi-CMOS inverters.**UNIT-III****Basic circuit concepts:** Sheet resistance Rs and its concepts to MOS, area capacitance calculations, inverter delays, driving large capacitive loads, wiring capacitances, fan-in and fan-out.**VLSI circuit design processes:** VLSI design flow, MOS layers, stick diagrams, design rules and layout, 2µm CMOS design rules for wires, contacts and transistors, layout diagram’s for NMOS and CMOS inverters and gates, scaling of MOS circuits, limitation of scaling.**UNIT-IV****Gate level design:**  Logic gates and other complex gates, switch logic, alternate gate circuits.**Physical design:**  Floor- planning, placement, routing, power delay estimation, clock and power routing **UNIT-V****Subsystem design:** Shifters, adders, ALUs, multipliers, parity generators, comparators, counters, high density memory elements.**VLSI design styles:** Full-custom, standard cells, gate-arrays, FPGAs and CPLDs and design approach for full custom and semi-custom devices.**UNIT-VI****VHDL synthesis:** VHDL synthesis, circuit design flow, circuit synthesis, simulation, layout, design capture tools, design verification tools.**Test and testability:** Fault-modelling and simulation, test generation, design for testability, built-in self-test. |
| **Text books** **&****Reference** **books:** | **Text books:**1.“Essentials of VLSI circuits and Systems”, by Kamran Eshraghian, Eshraghian  Douglas and A. Pucknell, PHI, 2005 edition.2.“Linear Integrated circuits”, by D. Roy Chowdhury, New Age International  Edition(2003)3.ASIC Design Flow by Smith.**Reference books:**1.“Principles of CMOS VLSI Design”, by Weste and Eshraghian, Pearson Education,  1999.2. “Modern VLSI Design”, Wayne Wolf, Pearson Education, 3rd Edition 1997.3.“Introduction to VLSI Circuits and Systems”, by John. P. Uyemura. John Wiley,  2003.4.“Digital Integrated Circuits”, by John M. Rabaey, PHI. |
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